

1. Signal amplification circuit (1) comprising a differential structure comprising two inputs (8, 9), a first (8), a reference voltage being applied to the first input, and the signal to be amplified being applied to the second input, the two inputs being biased to each other through a biasing resistor, the circuit being characterized in that the first input is also coupled to the second input through a signal resistance (3) through which the variations of which are representative of the signal to be amplified.
2. Circuit (1) according to claim 1, characterized in that the signal resistance (3) is initialized by the output of a microphone (2), the second input (9) of the amplifier (5) being designed to be connected to the output (4) from microphone (2).
3. Circuit (1) according to claim 1, characterized in that it comprises an impedance matching stage (11) comprising an input (12) connected to the first input (8), the reference voltage being applied to the second input (9) and the output (13) forming the first input of the differential structure amplifier (5).
4. Process for neutralizing noise in a microphone (2) power supply voltage, the noise from the microphone (2) being represented by a current in a current passing through a pu

2. Circuit (1) according to claim 1, characterized in that the signal resistance (3) is a pull-up resistance (3) initializing the operating position of a microphone (2), the second input (9) of the amplifier (5) being designed to be connected to an output (4) from microphone (2).

4. Process for neutralizing noise in an electret microphone (2) power supply voltage, an output signal from the microphone (2) being represented by variations in a current passing through a pull-up

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